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DES, encryption "general purpose register"

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[PS] Virtual Entity of Relevant Acronyms

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... item DEQNA Digital Ethernet Q-bus Network Adapter ethernet @item DEREPE Digital Ethernet REPeater ethernet @item **DES** Data **Encryption** Standard cryptography, NIST ...

surf.de.uu.net/bookland/inet/vera/vera-1.4/vera.ps - [Similar pages](#)

[PS] FINAL EVALUATION REPORT

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... is set, the corresponding **general-purpose register** is currently ... located in the **register file/sequencer** and ... ciphering unit - performs **encryption** and decryption ...

www.radium.ncsc.mil/tpep/library/fers/CSC-EPL-93-006.ps - [Similar pages](#)

Patent 5742840: General purpose, multiple precision parallel ...

... of devices to achieve **DES encryption**, Reed-Solomon ... video, radio, graphics, **encryption**, authentication, and ... can employ a **general purpose register** according to ...

www.freepatentsonline.com/5742840.html - 101k - Supplemental Result - [Cached](#) - [Similar pages](#)

Textsammlung

... Adapter DEREPE Digital Ethernet REPeater **DES** Data **Encryption** Standard DESPR ... 3] Generic Packetized Protocol (SAM) GPR **General Purpose Register** (CPU) GPSI ...

www.physik.tu-muenchen.de/~bbraun/texte/vera.htm - 101k - Supplemental Result - [Cached](#) - [Similar pages](#)

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... 1-11 Chapter 2 Altivec Register Set 2.1 Altivec **Register File**

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[XLS] Tabelle1

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... 2.2. 7. 5, 128...4096 Datenbytes. 1.3. 62. 6, 2,1-GBytes-Grenze **des** BIOS. 1.3. 66.

7, ... Wozu immer mehr Bits? 1.1. 24. 18, 8,4-GBytes-Grenze **des** BIOS. 1.3. 66. 19, ...

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... core contains a 16 entry, 32-bit **general purpose register file**, and processes ... crc 22805 Cyclic redundancy check **des** 510814 Data **Encryption** Standard dhry ...

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... on a Chip CSI Configurable System Interconnect CSL Configurable System Logic DCR
Device Control Register DCU Data Cache Unit **DES** Data **Encryption** Standard DISC ...

www.dcs.gla.ac.uk/publications/PAPERS/7214/TR-162.pdf - [Similar pages](#)

[VERA - Verzeichnis EDV-Relevanter Akronyme \[3/8\]](#)

... Fujitsu) FEK File **Encryption** Key (Verschlüsselung ... verein zur) Foerderung des
Oeffentlichen Bewegten und ... Protocol (SAM) GPR **General Purpose Register** (CPU) GPRS ...

www.uni-giessen.de/faq/archiv/de-acronyms.vera.part0-8/msg00003.html - 64k - [Cached](#) - [Similar pages](#)

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... verein zur) Foerderung des Oeffentlichen Bewegten ... GNU) GPR **General Purpose Register**
(CPU) GPRS ... IDEA International Data **Encryption** Algorithm (Verschlüsselung ...

www.uni-giessen.de/faq/archiv/de-acronyms.vera.part0-9/msg00004.html - 64k - [Cached](#) - [Similar pages](#)

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... Despite all the criticism, **DES** became the **encryption** standard in 1977. In 1983 it
was shown [1] that the cipher is vulnerable due to its short key length. ...

ce.et.tudelft.nl/publicationfiles/927_94_thesis.pdf - [Similar pages](#)

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... concepts can be found in **DES**, Twofish, etc. ... We modified the **encryption** instruction
putting focus on ... in both the pipeline and the **general purpose register file**. ...

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... It was shown in [15] that the addition of four custom instructions to the Xtensa
core can increase its **DES encryption** rate by a factor of between 43 and 72. ...

www.springerlink.com/index/KQV3KQ0E4U8YXDXA.pdf - [Similar pages](#)

[Efficient Permutation Instructions for Fast Software Cryptography](#)

... the standard data path arrangements of current microprocessors with a **general-purpose
register file**, as Figure 1 ... Speedup of **DES** for **encryption/decryption** (a ...

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... 71 5.3.2 Configuration **Register File** 72 5.9 Configuration
register file and associated configurations

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... for custom instructions. Besides the **general-purpose register file**, they allow some user-defined registers. Before custom instruction ...

www.princeton.edu/~fsun/files/iccad2003.pdf - [Similar pages](#)



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... FFT), convolvers, interpolators, and discrete-cosine transforms (DCT) that are required for video compression and decompression, **encryption**, convolution and ...

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